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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/675,396	09/30/2003	Ajay Kwatra	16356.823 (DC-05254)	6485
27683	7590	01/13/2009	EXAMINER	
HAYNES AND BOONE, LLP			DALEY, CHRISTOPHER ANTHONY	
IP Section			ART UNIT	
2323 Victory Avenue			PAPER NUMBER	
Suite 700			2111	
Dallas, TX 75219			MAIL DATE	
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

**Advisory Action  
Before the Filing of an Appeal Brief**

**Application No.**

10/675,396

**Applicant(s)**

KWATRA, AJAY

**Examiner**

CHRISTOPHER A. DALEY

**Art Unit**

2111

***--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --***

THE REPLY FILED 11/07/2009 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. ☐ The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.  
b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**NOTICE OF APPEAL**

2. ☐ The Notice of Appeal was filed on \_\_\_\_\_. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

**AMENDMENTS**

3. ☐ The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because  
(a) ☐ They raise new issues that would require further consideration and/or search (see NOTE below);  
(b) ☐ They raise the issue of new matter (see NOTE below);  
(c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or  
(d) ☐ They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_. (See 37 CFR 1.116 and 41.33(a)).

4. ☐ The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).  
5. ☐ Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.  
6. ☐ Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).  
7. ☐ For purposes of appeal, the proposed amendment(s): a) ☐ will not be entered, or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.  
The status of the claim(s) is (or will be) as follows:  
Claim(s) allowed: \_\_\_\_\_.  
Claim(s) objected to: \_\_\_\_\_.  
Claim(s) rejected: \_\_\_\_\_.  
Claim(s) withdrawn from consideration: \_\_\_\_\_.

**AFFIDAVIT OR OTHER EVIDENCE**

8. ☐ The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).  
9. ☐ The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).  
10. ☐ The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

**REQUEST FOR RECONSIDERATION/OTHER**

11. ☒ The request for reconsideration has been considered but does NOT place the application in condition for allowance because:  
See Continuation Sheet.  
12. ☐ Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). \_\_\_\_\_  
13. ☐ Other: \_\_\_\_\_.

/Christopher A Daley/  
Examiner, Art Unit 2111

/Khanh Dang/  
Primary Examiner, Art Unit 2111

Continuation of 11. does NOT place the application in condition for allowance because: when evaluating a claim for determining obviousness, all limitations of the claim must be evaluated. However, the references, alone, or in any combination, do not teach power means including (1) a power adapter connected to provide power to the second subsystem including the user console, whereby the power adapter is operable to receive an AC power input to be converted to various voltages as required by the user console, and (2) the second subsystem deriving power from the computer via a powerline of the Newcard device. The final office action fully meets the limitations mentioned. In particular, However, Fischer teaches a power adapter connected to provide power to the second subsystem including the user console, whereby the power adapter is operable to receive an AC power input to be converted to various voltages as required by the user console, and the second subsystem deriving power from the computer via a powerline. Fischer teaches in Figure 2 of a first subsystem 100 comprising power converter 104 and power connector 102 used to power a second sub system 58. Said power system 58, is with a device 10 comprising of a battery 60, and computer system 10. Said computer system is powered by battery 60 and power adapter 100 via powerline, power distribution system, COL. 8, lines 1 - 58. AC power is supplied to adapter 100 via socket 114N. Power converter 104 has the capability of deriving and providing a plurality of power to the computer system, COL. 7, lines 17 - 33. The Examiner points to the following teaching. Figure 2 as presented on page 8 of the remarks illustrates power adapter 100 comprising power converter 104 that derives the requisite power needed for said system, COL. 7, lines 17 - 33. Further, as clearly discussed above, the NewCard, also known as ExpressCard, by definition, consists of two I/O buses (PCI-Express and USB), and power is supplied to the card by the host system via the PCI-E and/or USB power interface/pins. Specifically, the Newcard draws 3.3 volts from the host via power interface of the PCI-E and/or USB power interface. For example, in case of USB power interface, a voltage supply is provided in a wire or "line" as dictated by the USB specification. Thus, it is clear that power is derived from the computer system via a power line or wire of the NewCard consisting of two I/O buses, namely the PCI-E bus and the USB bus. Thus the rejection is maintained.